



Modelling of Four-Port Converter for Electric Vehicle Applications

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Abstract: *Multiport Converters (MPC) combine various renewable power sources through a single-stage power conversion. They offer the benefits of centralized control, excellent reliability, easy circuit configuration, small packaging, and cheap manufacturing cost & size. Due to its great efficiency and dependability, using a Multiport Converter to include renewable energy & energy storage into the bipolar dc bus is a preferable alternative. Full-bridge interleaving bidirectional buck/boost semi active rectifier with bi-polar output converter, a typical four-port converter (FPC), is specifically and in-depth examined in terms of operation principles, voltage and power relationship, port current ripples, soft switching performance, & control. The modelling and simulation studies of the FPC for PV, battery, and bipolar outputs in MATLAB/Simulink suggested in this paper. Effects of duty ratio at different battery voltage and battery charging and discharging at various duty ratio verified. The power flow analysis done for different intensities of PV and Battery voltage and load conditions.*

Keywords: *Multiport Converter, Electric Vehicle, DC – DC Converter, PV and Battery*

1. INTRODUCTION

The popularity of DC distribution networks is rising. Because of improvements in power electronic technology and the expansion of sectors with dc loads, like as data centres and battery electric automobiles, electric vehicles (EVs) will eventually overtake other modes of transportation as the preferred choice [1]. Modern DC systems may do considerably more complex tasks than only voltage regulation. As a result, they are gaining popularity as a solution for a variety of domestic and commercial applications, including DC-powered residences, quick EV charging places, hybrid energy storage systems (ESS), & renewable energy parks.

Different types of renewable energy sources (RESs), such as PV, wind and geothermal, are the promising clean energy sources for the future. Due to the unpredictability of these RES,



ESS, like batteries & super capacitors, have been frequently deployed in conjunction with renewable energy sources. Energy storage enables generation sources to operate at maximum efficiency while accommodating variations in demand without the need to ramp up or down. As a result, system stability and power quality can be guaranteed. To control power fluctuations and reduce electricity costs, the RESs' redundant energy may be stored in the ESS [2]. Lithium-ion batteries have drawn a lot of attention recently since they are an energy storage solution that has both high energy density and long-lasting.

Onboard energy storage systems have received a lot of attention lately due to a rise in interest in the applications that make use of them, such as electric automobiles, electrified railway vehicles, electric ships, electric boats, and electric aircrafts [3]. Batteries, ultracapacitors, fuel cells, flywheels, or a combination of them, can serve as the foundation for these energy storage systems.

The two main categories of EV charging station architectures are common ac bus and common dc bus. The latter appears to be preferable because it requires fewer conversion stages to achieve a greater system efficiency and makes it simpler to integrate storage systems (batteries or ultra-capacitors) RES. Using three-level neutral-point-clamped (NPC) converters, the conventional dc-bus design can be implemented as bipolar dc bus systems [4]. One of the greatest popular is the bi-polar DC Bus system. 2 wires, one with +ve polarity and the other with -ve polarity, make up a bi-polar DC bus. The current through ground is 0 in normal operation. With the right management measures, a metallic return is also conceivable. The following benefits of this topology outweigh its greater technical complications, expense, and dependability over the unipolar DC: During normal process, the current via the return wire is lower, which reduces power losses; if a fault develops on one bus, the operation of the other buses is unaffected and continues to be normal [5]-[6]. By charging and discharging the corresponding capacitors, the balancing circuit would preserve the voltages on the positive and negative poles. Given that the current drawn is decreased, this is advantageous when certain loads require a lot of electricity. When transferring the same amount of power, the current flowing in a bipolar dc network can be cut in half compared to the one in a unipolar network [7]-[10]. The aim of this paper is to influence the efficiency boundaries of the multi-port converter-based EV charging station.

A multiport converter is preferable to numerous single-input converters in a freestanding renewable energy system because it has the advantages of a simpler circuit, higher efficiency, and cheaper cost. Multiple input and/or output ports on multi-port converters allow for the connection of power supplies and electric loads. The converter regulates the flow of electrical energy between power sources and loads. Every port has the ability to transfer energy in both directions [11]-[15]. An MPC acts as a single power converter and it connects a number of elements at different port. It consists of a number of sources, storage ports and loads connected to it. Bidirectional power flow paths are also possible in this structure. Since MPC acts as a single power processing unit, a centralized power control system can be used which eases the control complexity [15]-[20].

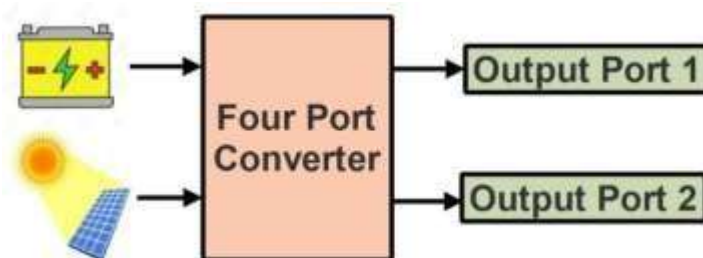


Fig.1 Structure of Four-port converter

The four-port converter integrates two inputs and two outputs. This topology is utilized for DC bus application and thus identification of ports for a simple four port application is as follows;

1. One of the ports at the primary side is chosen as a PV power source.
2. Another port at the primary side is chosen as a storage source (Battery).

The last two ports are the bipolar load port.

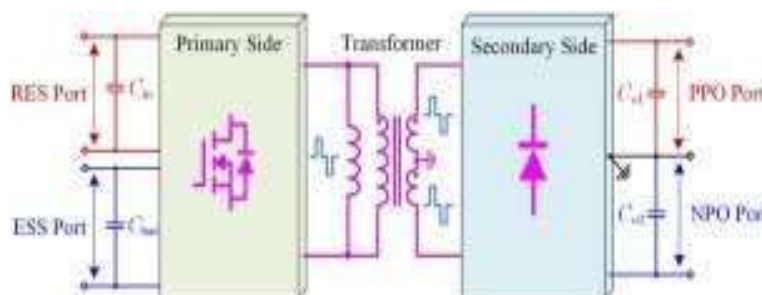


Fig.2 The proposed isolated four-port converter's basic design

In this paper, the Full Bridge Interleaving Bidirectional Buck Boost + Semi Active Rectifier (FB-IB3+SAR-BO) converter is designed and simulation study is carried out. The PV source and Battery storage system are the input source of the Four Port Converter (FPC). The performance of the converter is investigated for different solar intensities and battery voltages. The effect of Duty ratio is studied and optimum duty ratio for regulated output of 60 V is identified and the results are discussed.

Proposed Topology of Isolated Four Port Converter

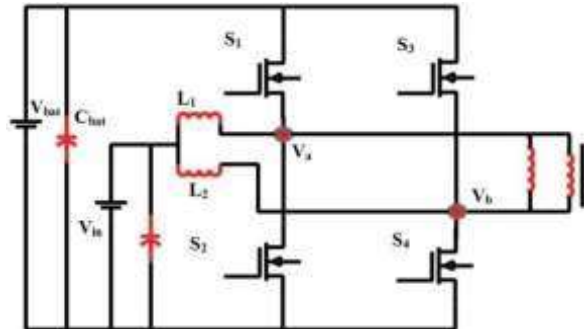


Fig. 3 Topology of the proposed Circuit (Primary Side)

In the primary side, Topology converter circuit is utilized to reverse the input potential of RES and ESS in the large frequency AC voltage being delivered to the Center-tapped transformer. It consists of Four switches $S_1 - S_4$ and two inputs such as PV power source and a Battery.

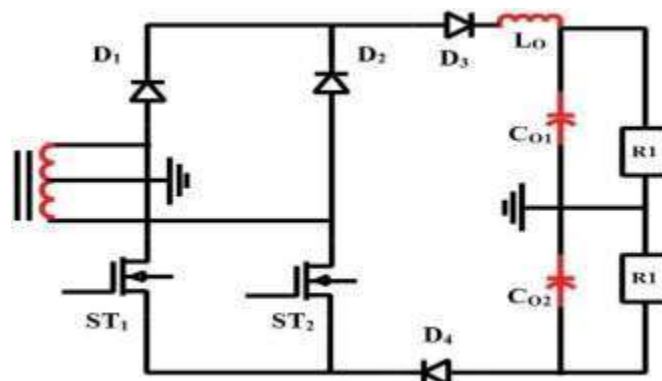


Fig. 4 Semi-Active Rectifier with Bipolar Outputs

The Secondary side of proposed converter, the topology is Semi-Active Rectifier (SAR-BO) is considered to rectify the high-frequency AC voltage into Bipolar DC output. It consists of two switches and four diodes $D_1 - D_4$ with Bipolar outputs.

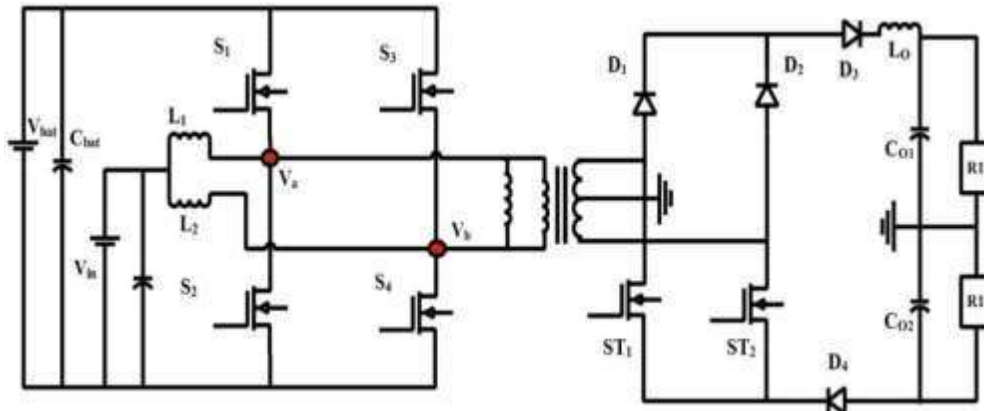


Fig. 5 Proposed converter complete circuit

Proposed Converter Description

Because of the extensive soft switching choice and limited count of semiconductors and magnetic elements, FB-IB3 and SAR-BO were chosen as the primary and secondary side circuits, respectively. The interweaving bi-directional buck/boost circuit with two inductors (L_1 and L_2) and four switches S_1 – S_4 makes up the primary side. S_1 and S_2 (S_3 and S_4) have complementary driving signals. The currents of inductors L_1 and L_2 are i_{L1} and i_{L2} , correspondingly, and the current of the RES is i_{in} . The two switch legs are powered in an interleaved fashion with a 180° phase shift angle, which helps to decrease the RES port's input current ripple. The range of D is controlled by the voltages of the RES and ESS ports, and the duty cycle of S_1 is set as D . The primary and secondary sides of the transformer have a turn ratio of $1:n:n$. ST_1 and ST_2 are driven complementarily on the secondary side of the converter with a fixed duty cycle of 0.5. Depending on the primary side winding current i_p , the proposed circuit can function in continuous current mode (CCM) or discontinuous current mode (DCM). The operation of the converter under the CCM is explored and analyzed in depth in this article. In this scenario, one switching period contains 14 operation stages. Only

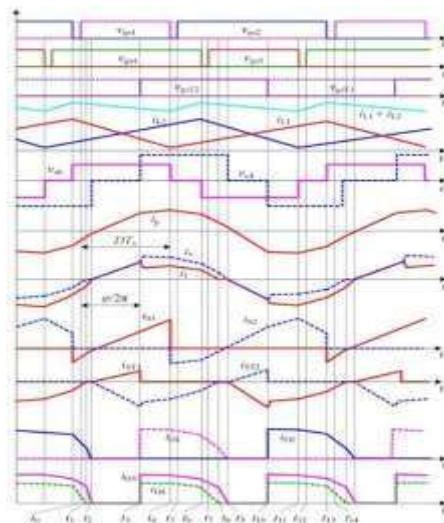


Fig. 6 Steady state waveforms of the proposed converter

seven operating steps in a half switching time are shown due to circuit symmetry. The essential waveforms are depicted, as well as the comparable circuits in each switching level. The following is a detailed operational examination of each mode. v_{gs1} – v_{gs4} are the driving signals of switches S_1 – S_4 , where S_1 and S_2 (S_3 and S_4) have complimentary driving signals and the driving signals of S_1 and S_3 have a 180° phase shift. Furthermore, v_{gsT1} – v_{gsT2} are the driving signals for the switches ST_1 – ST_2 , which are operated complementarily with a set duty cycle of 0.5. V_{ab} and v_{cd} , correspondingly, are the potentials amongst the center of the primary and secondary side rectifier legs. The primary side winding N_p , secondary side winding N_s , and third side winding N_t , respectively, have currents of i_p , i_s , and i_t . The currents of diodes D_1 – D_4 are denoted by i_{D1} – i_{D4} .

Operation Modes

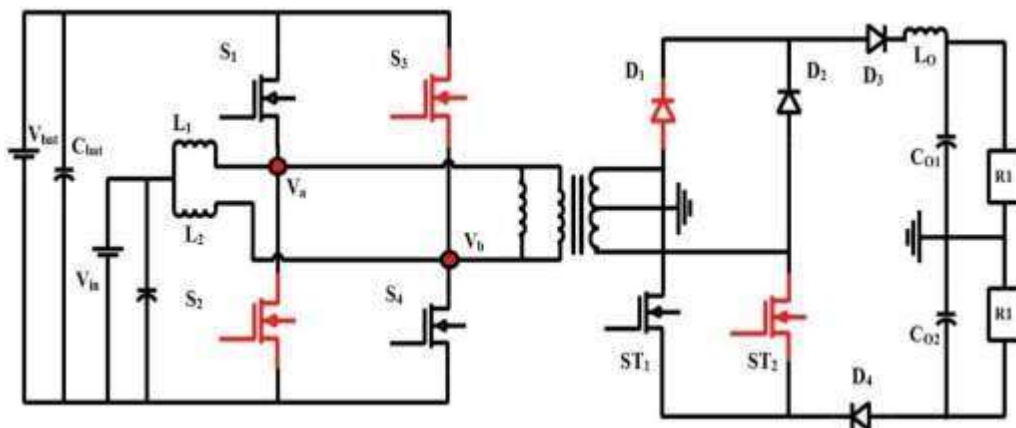


Fig. 7 (a) Operation of Mode 1

In Fig 7(A), S_4 and ST_1 are turned on before t_0 , at the interval between S_2 and S_1 . Through S_4 and S_1 's body diode, the primary side winding current i_p passes, the inductor L_1 is linearly de-energized, i_{L1} is linearly decreasing. With the voltage V_{in} , the inductor L_2 is linearly activated, and i_{L2} is linearly growing. The secondary side windings N_s and N_t send power to the NPO and PPO ports via ST_1 and D_4 , D_2 , and D_3 , while the primary side winding current i_p is calculated as $i_p(t) = i_p(t_0) + (V_{bat} + V_{o1}/n)(t-t_0)/L_k$.

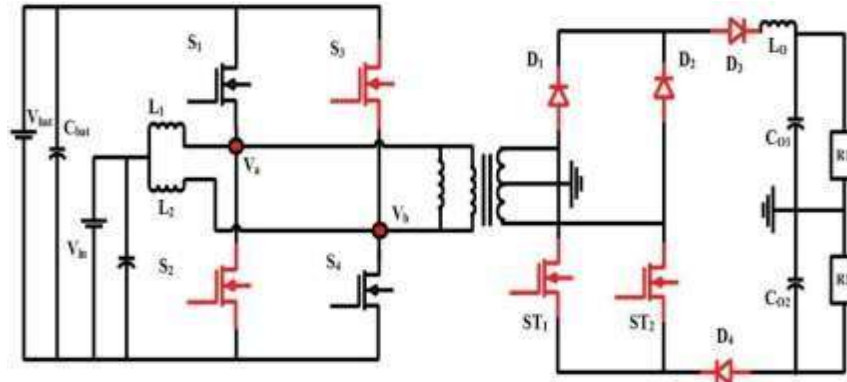


Fig. 7(b) Operation of Mode 2

In Fig 7(B), The PPO and NPO ports load currents meet $I_{o1} > I_{o2}$, and the diode currents meet $i_{D3} > i_{D4}$. The diode current i_{D4} and the secondary side winding N_s currents are both reduced to zero at time t_1 . In this stage, i_{L1} and i_{L2} are the same as in the previous one. This stage will come to a close at t_2 when i_{D2} and i_{D3} reach zero.

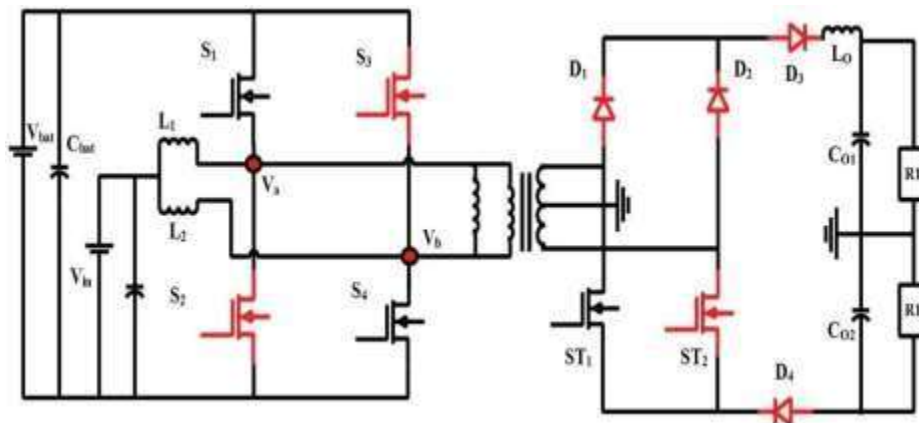


Fig. 7(c) Operation of Mode 3

In Fig 7(C), The secondary side winding current is freewheeling at this point, just passing through ST_1 and the ST_2 body diode, and no energy is supplied from the secondary side to the bipolar load. $i_p(t) = i_p(t_2) + V_{bat}(t-t_2)/L_k$ can be written as $i_p(t) = i_p(t_2) + V_{bat}(t-t_2)/L_k$ at this stage.

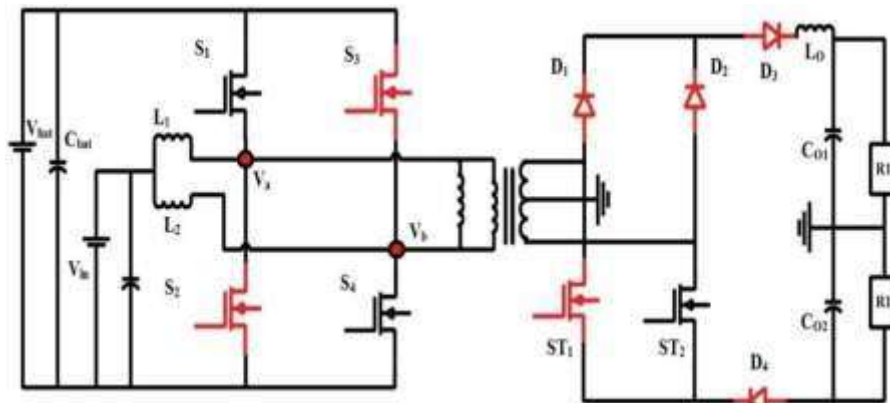


Fig. 7(d) Operation of Mode 4

In Fig 7(D), ST1 is switched off and ST2 is switched on at time t_3 . Through D1 and D3, ST2 and D4, the secondary side windings N_s and N_t deliver power to the PPO and NPO ports, respectively. V_{bat} and $V_{o1} + V_{o2}$ are the primary potential v_{ab} and secondary potential v_{cd} , respectively.

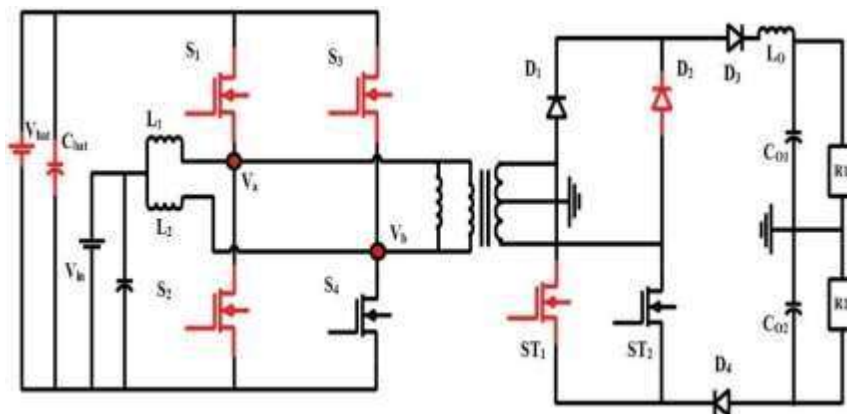


Fig. 7(e) Operation of Mode 5

In Fig 7(E), S4 is turned off at t_4 . With the voltage V_{in} , the inductors L_2 are linearly powered, and i_{L2} remains to rise linearly. With the voltage V_{in} , the inductors L_1 begin to be linearly powered, and i_{L1} begins to increase linearly. Through D1 and D3, ST2 and D4, the secondary windings N_s and N_t deliver power to the ports, correspondingly.

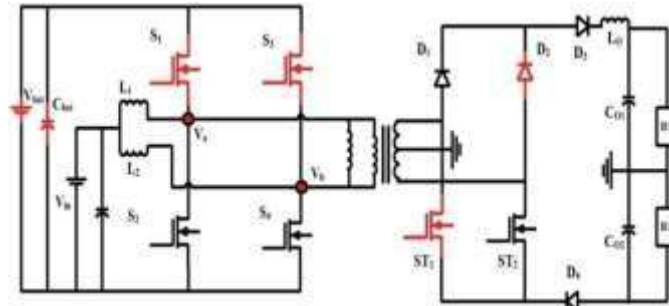


Fig. 7(f) Operation of Mode 6

In Fig 7(F), S2 is switched ON, at t_5 . The proposed circuit functions similar as 4th phase of operation.

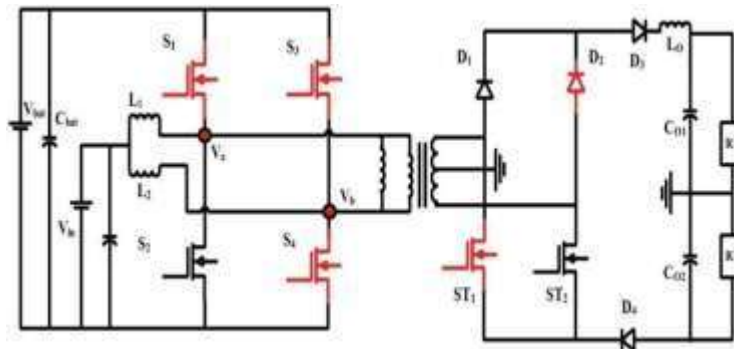


Fig. 7(g) Operation of Mode 7

In Fig 7(G), S4 is turned off at t_6 . Through D1 and D3, D4 and ST2, the secondary windings deliver power to the ports.

Specification of Proposed Converter

Table1: Specification of the converter

Parameter	Value	Parameter	Value
Output voltage (V_0)	$\pm 60v$	Turn ratio n	$n=0.9$
PV voltage (V_{in})	30V	Magnetic Inductor L_m	120 μ H
PV current (I_{in})	0-10A	Leakage Inductor L_k	7 μ H
Inductor L_1 and L_2	85 μ H	Battery voltage	96V
Capacitors C_{bat} and C_{in}	220 μ F/100v	Switching frequency	100Khz
Capacitors C_{o1} and C_{o2}	330 μ F/150V	Diodes $D_1 \sim D_2$	STPS401120CT

2. SIMULATION RESULTS AND DISCUSSION

The simulation results of the FB-IB3 and SAR-BO proposed converter are discussed. The peak power of the panel at the standard conditions of temperature 25 °C and intensity 1000 W/m² is 250 Watts. Fig 8 (a) The I-V and (b)V-P characteristics of the panel are presented.

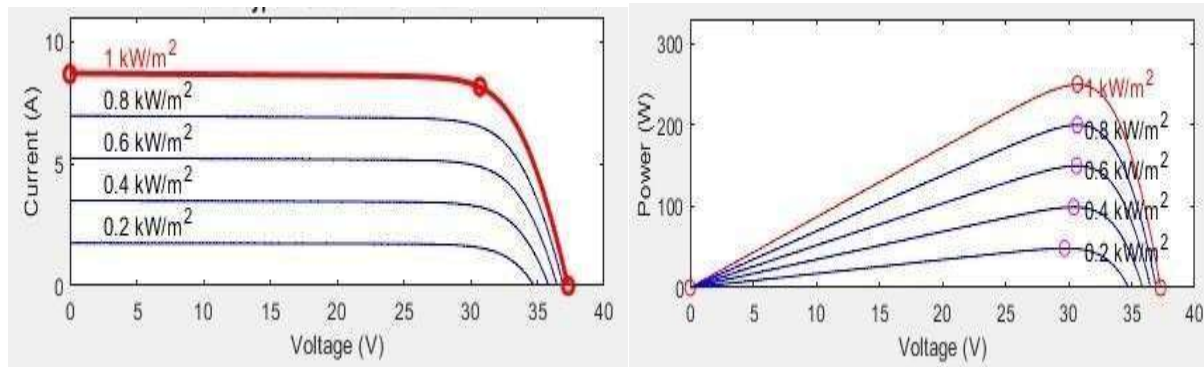


Fig. 8 VI and PV Characteristics of the Solar Panel

Battery charging and discharging

Battery = Lead acid Battery

Nominal voltage = 96 Volts

Battery rated capacity = 100 Ah

Initial State of Charge = 80

Simulation Results at rated condition and the waveforms

Intensity = 1000 W/m²

Battery voltage = 96Volts

Rated PV voltage VPV = 30 V

Rated PV current IPV = 0-10 A

Rated Load voltage V0 = 60 V

Rated Load current I0 = 12 V

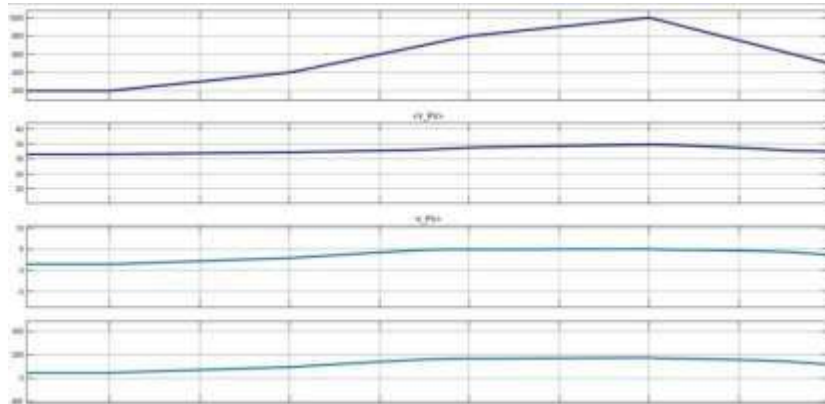


Fig. 10 Varying Solar intensities at duty ratio 0.5 with solar output

The following figures are the simulation study of Gate pulse for switches (S1-S4), ST1 and ST2 at rated condition



Fig. 11 Gate pulse for Switch S1 and S2 for 0.3 duty ratio

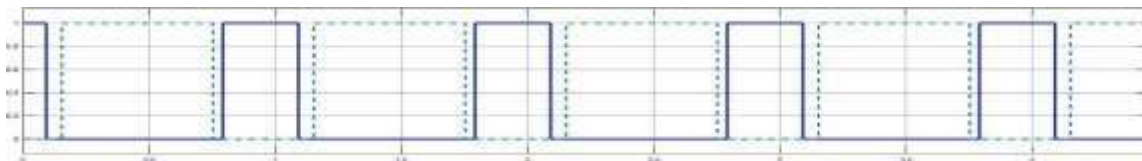


Fig. 12 Gate pulse for Switch S3 and S4 for 0.3 duty ratio



Fig. 13 Gate pulse for Switch ST1 and ST2 for 0.5 duty ratio

The following Fig 14 (a)-(h) are the simulation study of intermediate of the proposed circuit at rated condition



Fig. 14(a) The current of inductors L1 (i_{l1}) and L2 (i_{l2}), $i_{l1} = 3.6$ A, $i_{l2} = 3.8$ A



Fig. 14(b) The primary voltage $V_{ab} = 98$ V, and the secondary side voltage $V_{cd} = 140$ V



Fig. 14(c) The current in the primary side circuit is $i_p = 21$ A



Fig. 14(d) The current in the secondary side circuit is $i_s = 21$ A, both is it synchronizes.



Fig. 14(e) The current through the switches S_1 and S_2 $i_{s1} = 22$ A, $i_{s2} = 20$ A

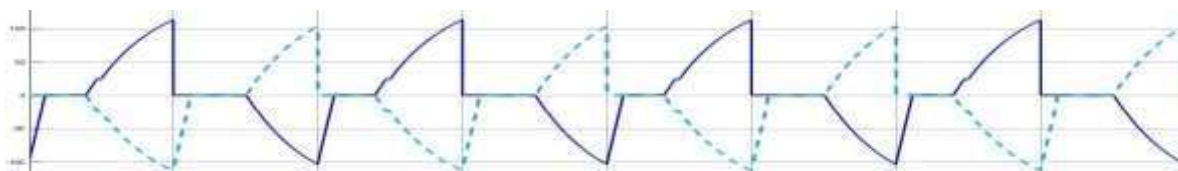


Fig. 14(f) The current through the switches ST_1 and ST_2 $i_{st1} = 100$ A, $i_{st2} = 100$ A

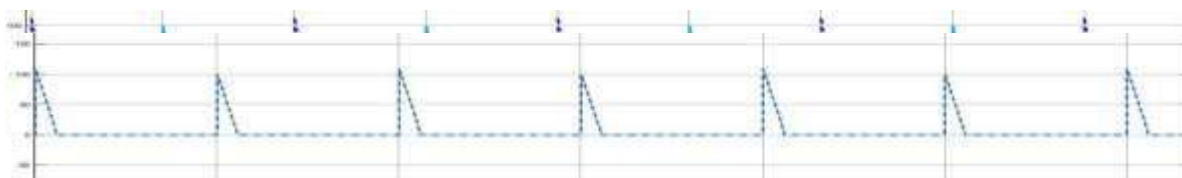


Fig. 14(g) The current through the Diodes D_1 and D_2 $i_{d1} = 102$ A, $i_{d2} = 100$ A

Fig. 14(h) The current through the Diodes D_3 and D_4 $i_{d3} = 100$ A, $i_{d4} = 100$ A, both i_{d3} i_{d4} synchronizes.

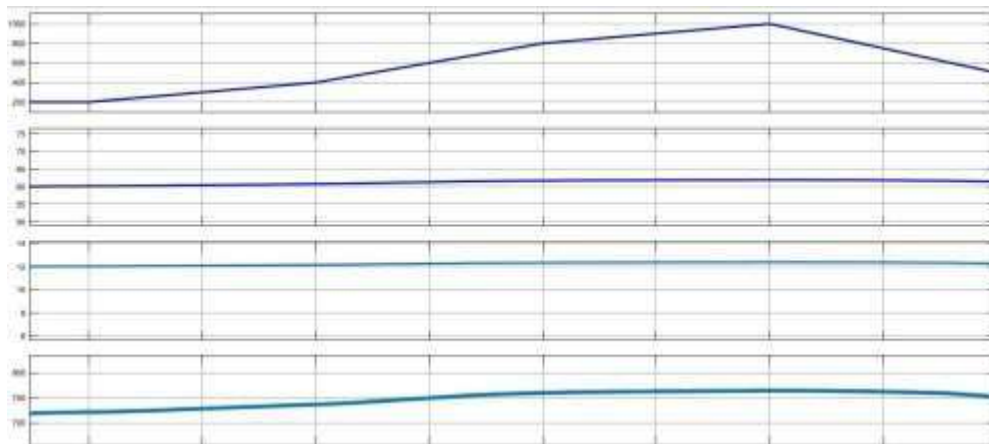


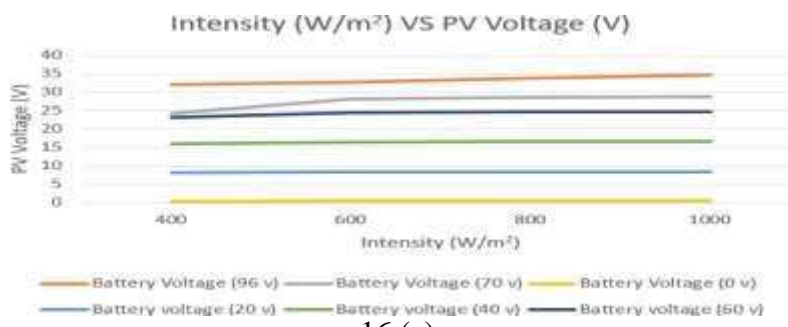
Fig. 15 varying solar intensities with bipolar outputs at rated duty ratio 0.5

Simulation results for different Battery voltage

Solar panel output for various Intensities

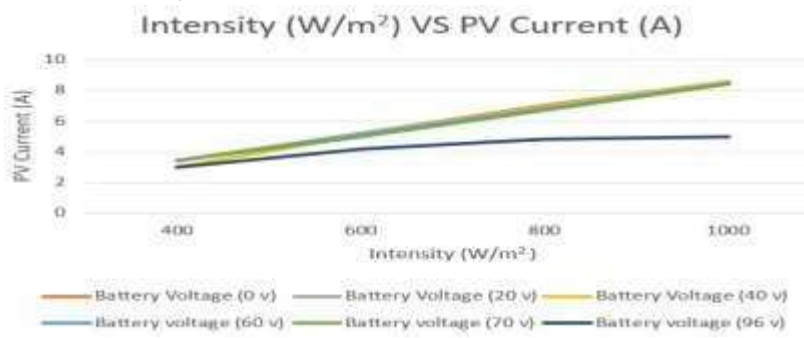
The solar PV panel output such as PV voltage, PV current, PV power for fixed duty ratio of 0.5 are plotted against various Intensities for different battery voltage such as 96 v, 70v, 60 v, 40 v, 20 v, 0 v.

Intensity vs PV Voltage V_{PV}



16 (a)

Intensity vs PV Current I_{PV}



16 (b)

Intensity vs PV Power P_{pv}

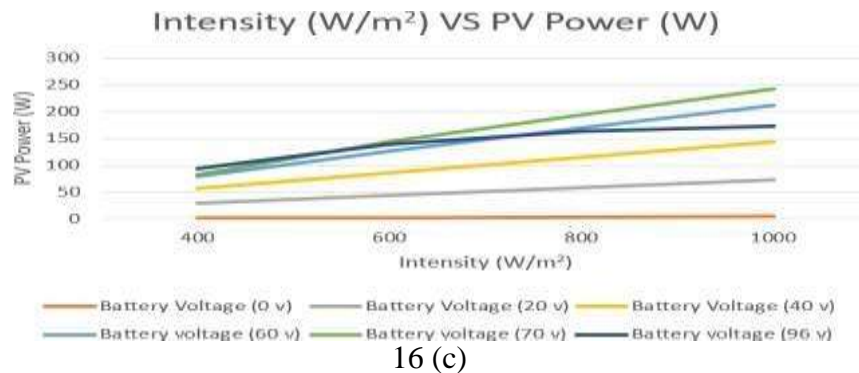
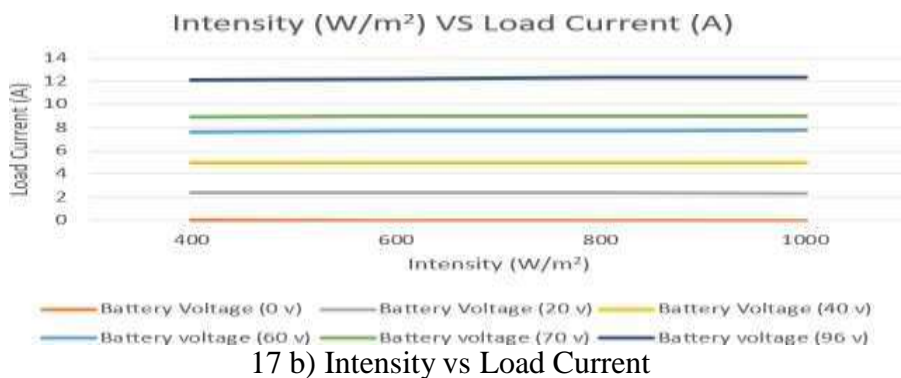
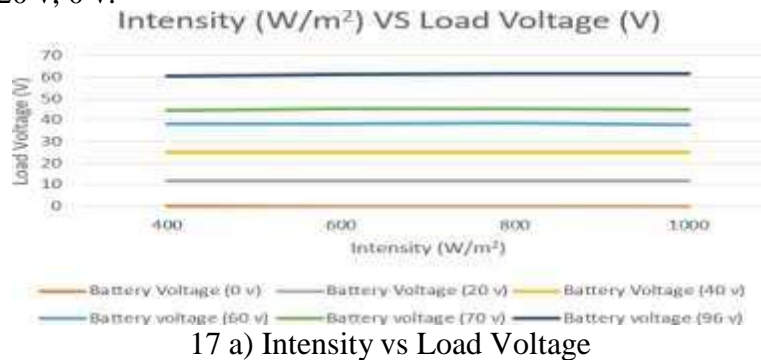


Fig. 16 Solar panel output (a) Voltage VPV (b) Current IPV (c) Power PPV

Bipolar output for various Intensities

The obtained Bipolar Output such as Load voltage, Load current, Load power for fixed duty ratio of 0.5 are plotted against various intensities for different battery voltage such as 96 v, 70v, 60 v, 40 v, 20 v, 0 v.



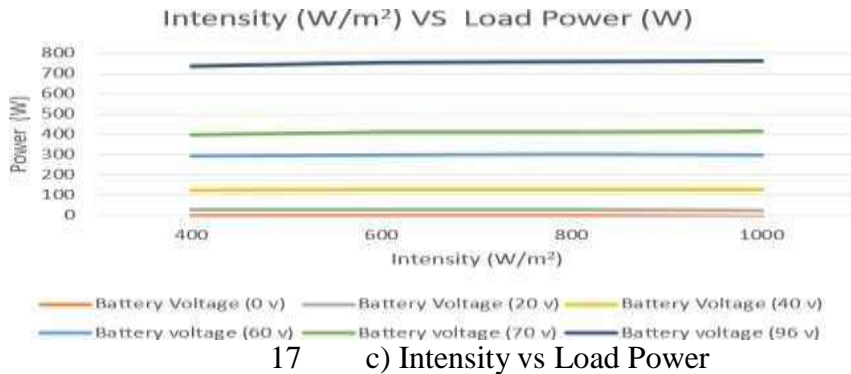
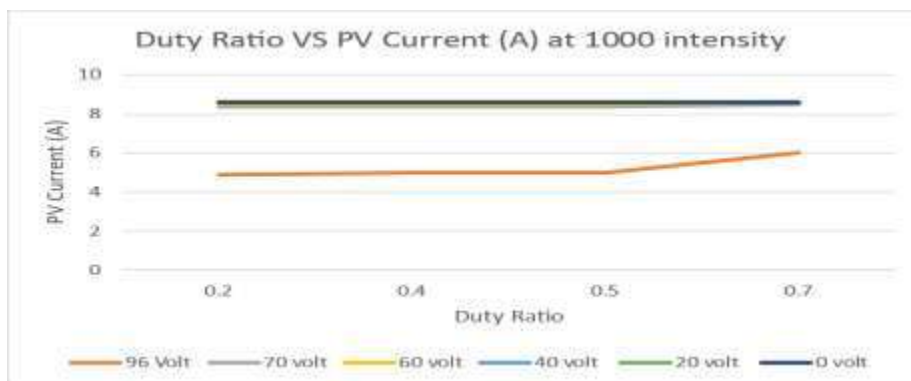
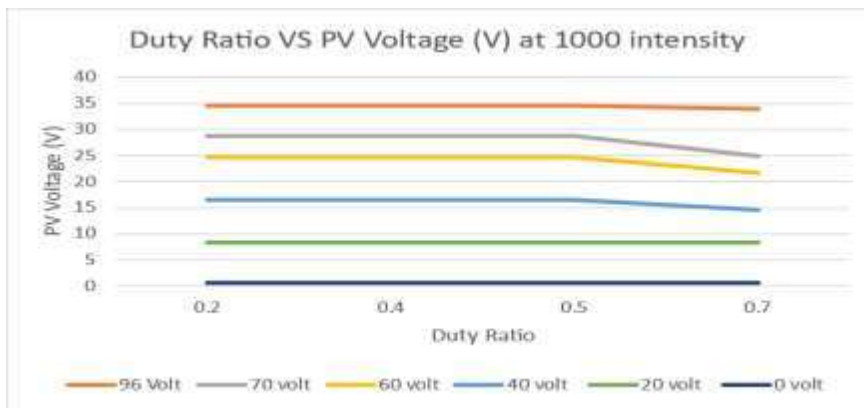
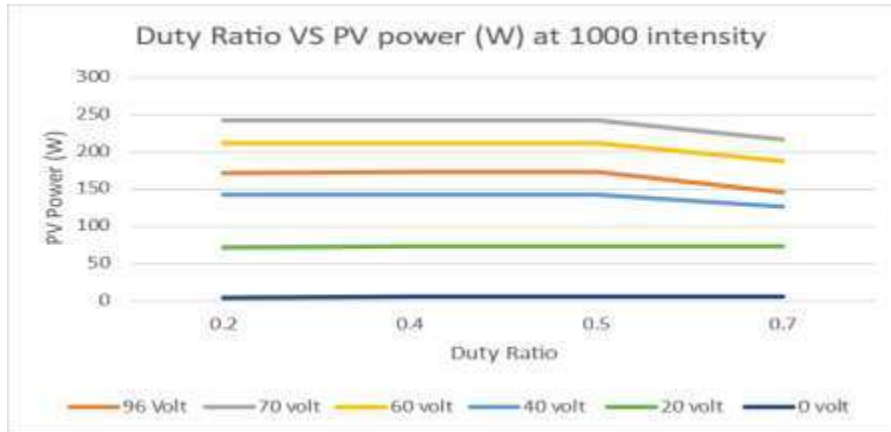


Fig 17 Bipolar output (a) Output Voltage V₀, (b) Output Current I₀, (c) Output Power P₀

Effects of Duty Ratio at intensity 1000 W/m²

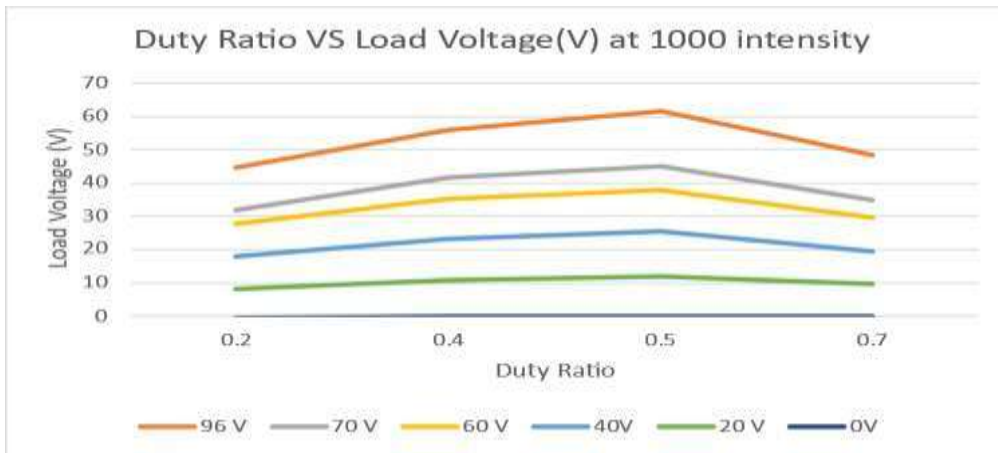
For intensity 1000, plotting duty ratio vs solar panel output and bipolar output.



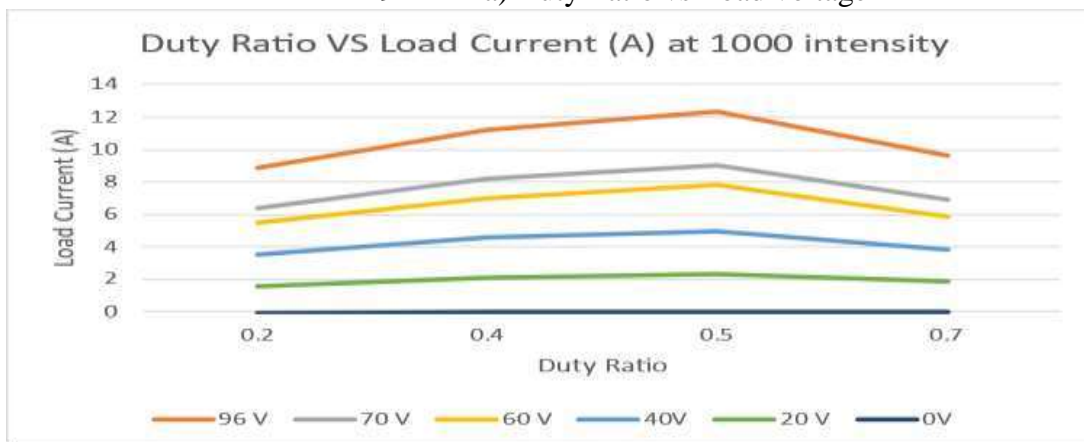


18 c) Duty Ratio vs PV power

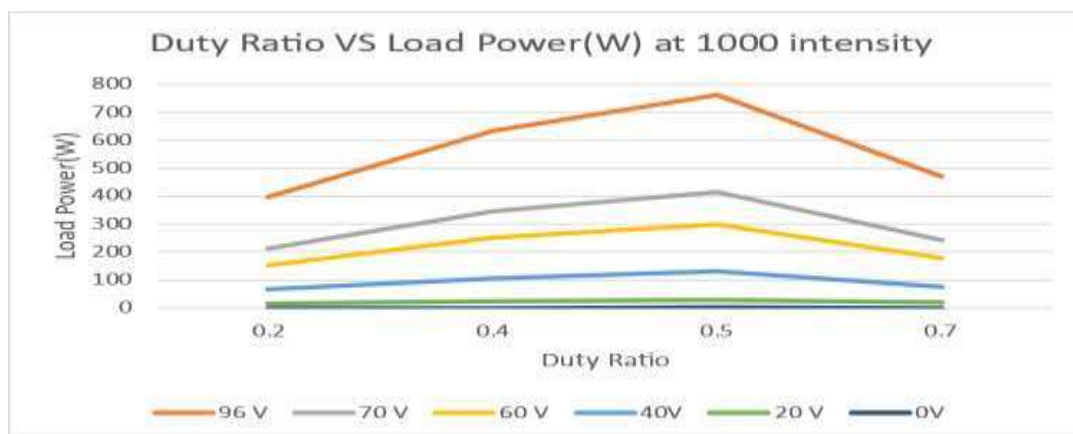
Fig. 18 At 1000 W/m² Duty ratio vs solar output, (a)PV voltage VPV, (b) PV current IPV, (c)PV power PPV.



19 a) Duty Ratio vs Load voltage



19 b) Duty Ratio vs Load current



19 c) Duty Ratio vs Load power

Fig. 19 At 1000 W/m² Duty ratio vs bipolar output, (a) Load voltage V₀, (b) Load current I₀, (c) Load power P₀

From the Solar PV panel output such as PV voltage, PV current, PV power, for various duty ratio the PV voltage remains constant for each battery voltage. The PV power increases for every Battery voltage.

From the Bipolar output, it can be seen that load voltage, load current and load power at all intensities reaches Maximum at 0.5 Duty ratio, the load voltage 61.7 V, load current 12.3 A, and the load power 761 W for 0.5 duty ratio

3. CONCLUSION

In this paper, a 4-port converter built on center tapped winding for bi-polar DC bus usages is studied through simulation. When compared to the traditional converter, multi-port converter (MPC) acts as a single power converter and it connects a number of elements at different port, a four-port converter (FPC) said to occupied its place viewing diversified merits. with this viewpoint, the proposed topology is considered in this project. Then over-all assembly of isolated 4-port converter circuit output is explained in detailed.

The essence of the topology of the FB-IB3 and SAR-BO and the circuit diagram of the proposed converter are presented. The simulation results pertaining to the PV panel, the I-V and P-V characteristics. The battery charging and discharging characteristics has been discussed. The simulation results at rated condition and the steady-state waveforms are analyzed and discussed. The simulation results of Solar PV panel output, Bipolar output is plotted against various intensities at different battery voltage are obtained and also the Effects of duty ratio at various intensities are obtained and it has been observed that Bipolar output reaches maximum at 0.5 Duty ratio. The battery charging and discharging at different duty ratio has been observed and all the simulation results are verified and discussed.

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